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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Tadashi OSHIMA**

Serial No.: **Not Yet Assigned**

Filed: **November 30, 2001**

For: **SEMICONDUCTOR DEVICE WITH TWO TYPES OF FET's HAVING DIFFERENT GATE LENGTHS AND ITS MANUFACTURE METHOD**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

November 30, 2001

Sir:

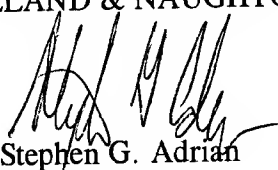
In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,
ARMSTRONG, WESTERMAN, HATTORI
MCLELAND & NAUGHTON, LLP


Stephen G. Adrian
Reg. No. 32,878

Atty. Docket No.: 011318
Suite 1000, 1725 K Street, N.W.
Washington, D.C. 20006
Tel: (202) 659-2930
Fax: (202) 887-0357
SGA/II
Enclosures: PTO-1449; References (6)